

INFORMATION DISCLOSURE CITATION	ATTY. DOCKET NO.	SERIAL NO.
	<u>550-489</u>	<u>To be assigned</u>
	APPLICANT	
	<u>FLYNN, D.</u>	
(Use several sheets if necessary)	FILING DATE	GROUP
	<u>November 19, 2003</u>	

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

MJB	20002/0140467 A1, US Patent Application Publication of NAFFZIGER ET AL.; "SYSTEM AND METHOD UTILIZING ON-CHIP VOLTAGE CONTROLLED FREQUENCY MODULATION TO MANAGE POWER CONSUMPTION"; October 3, 2002.
MJB	The Technology Behind Crusoe Processors, by Alexander Klaiber, Transmeta Corporation, January 2000, pages 1-17.
MJB	Longrun Power Management, by Marc Fleischmann, Transmeta Corporation, 17 January 2001, pages 1-17.

*Examiner

Michael Q.b

Date Considered

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INFORMATION DISCLOSURE CITATION	ATTY. DOCKET NO.	SERIAL NO.
	550-489	10/715,368
	APPLICANT	
	FLYNN	
(Use several sheets if necessary)	FILING DATE	TC/A.U.
	November 19, 2003	2817

(Use several sheets if necessary)

SEP 07 2004

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

*Examiner

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Date Considered

217106

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